IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

Hidenori KAWANISI

Application No.:

09/466,174

Filed:

December 17, 1998

Group:

2825

Examiner:

Not Assigned

For:

SEMICONDUCTOR LASER DEVICE WITH SPOT-SIZE

CONVERTER AND METHOD FOR FABRICATING THE SAME

LETTER

Assistant Commissioner for Patents Washington, DC 20231

February 9, 2001

Sir:

Under the provisions of MPEP § 2001.06(b), the Examiner is hereby advised of the following co-pending U.S. Application(s):

Application No.	Filing Date	Group
09/667,775	September 22, 2000	Unknown⊣
09/670,820	September 28, 2000	2881 FEB 13

The subject matter contained in the above-listed co-pending U.S. application (s) be deemed to relate to the present application, and thus may be material to the present application of this instant application.

The above-listed co-pending application(s) is(are) not to be construed as prior art. By bringing the above-listed application(s) to the attention of the Examiner, Applicant(s) do(does) NOT waive any confidentiality concerning the above-listed co-pending application(s) or this application. See MPEP § 101. Furthermore, if said application(s)

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should not mature into patents, such application(s) should be preserved in secrecy under the provisions of 35 U.S.C. § 122 and 37 C.F.R. § 1.14.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By

DJD:kna

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